

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: F. ITO

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application  
Serial No. 09/870,726, filed June 1, 2001

Filed: On Even Date Herewith

For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND A METHOD OF MANUFACTURING THE SAME

Art Group of Parent: 2813

Examiner of Parent: L. M. Schillinger

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 21, 2004

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Specification**

**Remarks are included following the amendments.**